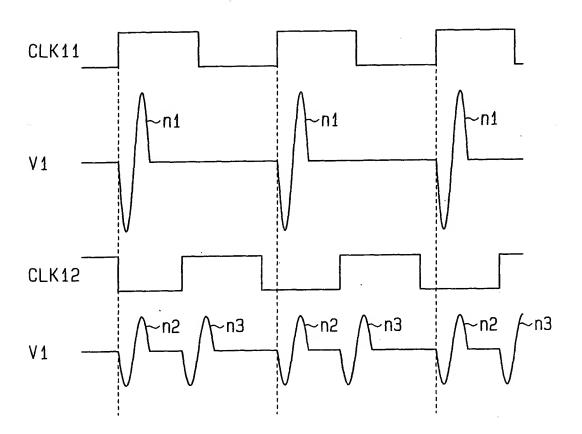
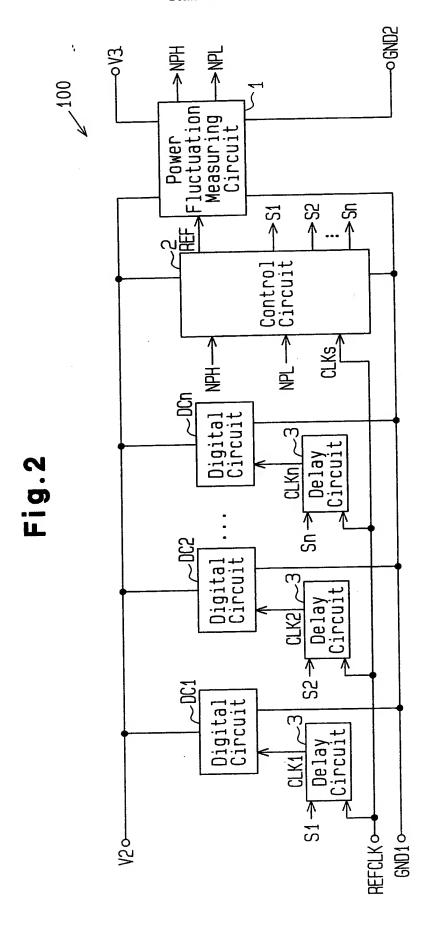
Fig.1 (Prior Art)





Title: DEVICE AND METHOD FOR INHIBITING POWER FLUCTUATION Inventor: Shigetaka ASANO

Inventor: Shigetaka ASANO Docket Number: 108075-00117

Fig.3

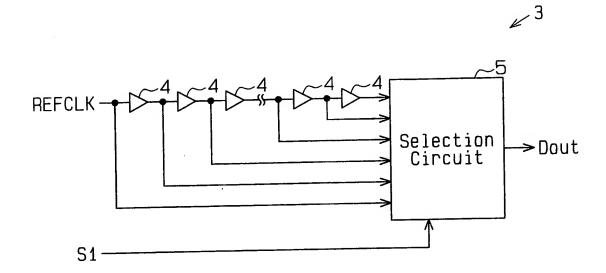
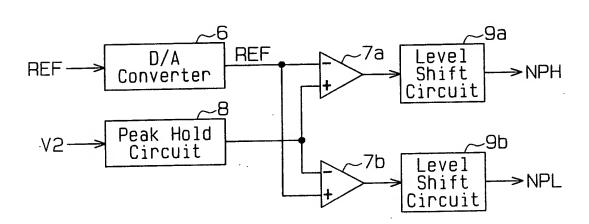


Fig.4

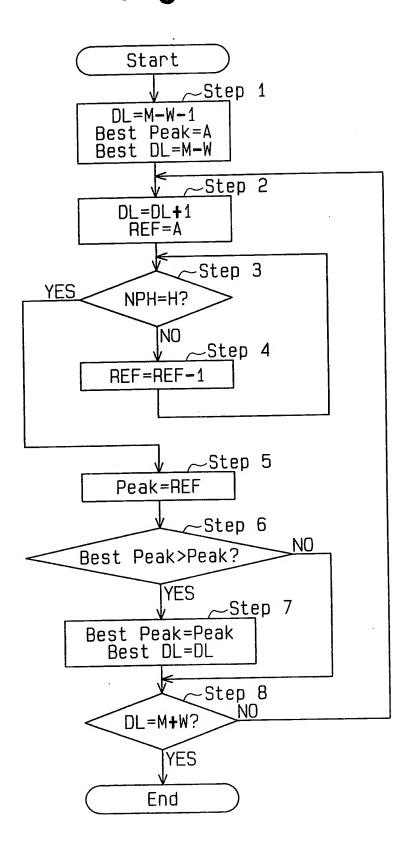
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Inventor: Shigetaka ASANO Docket Number: 108075-00117

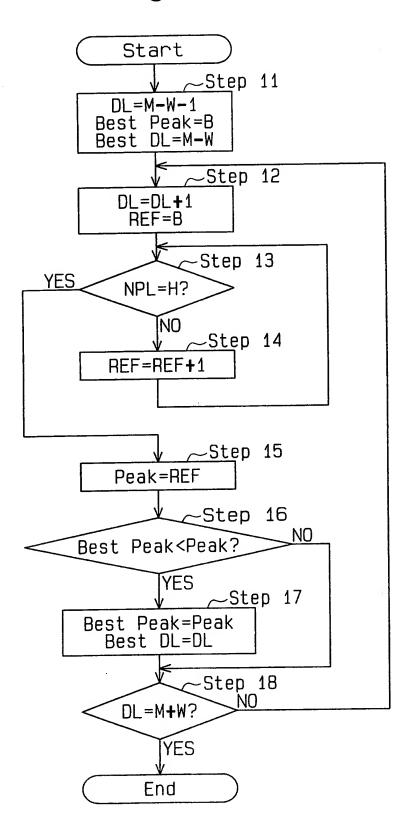
Fig.5



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Fig.6



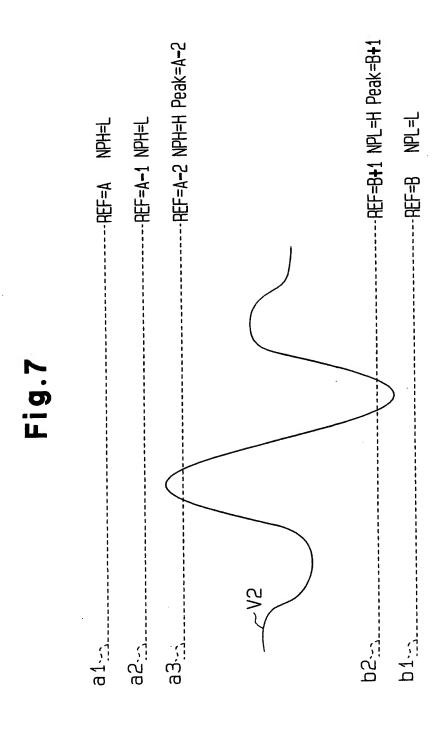
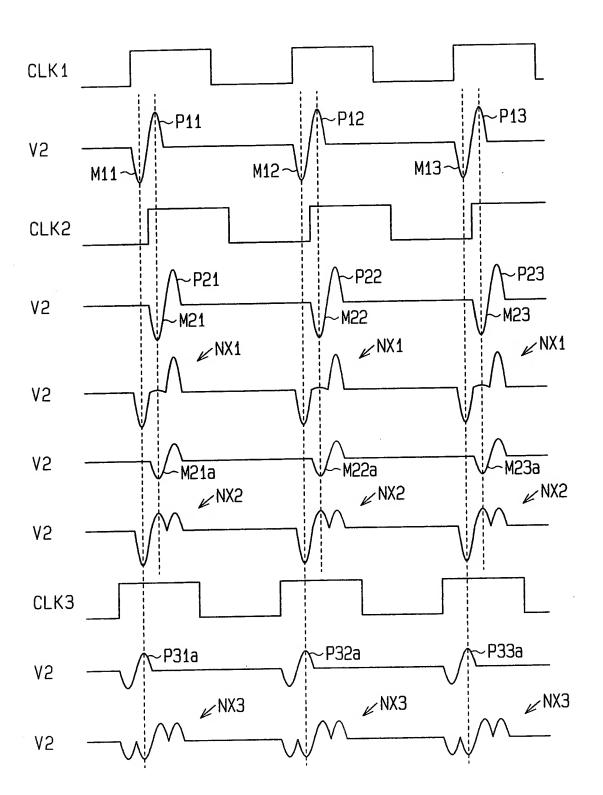
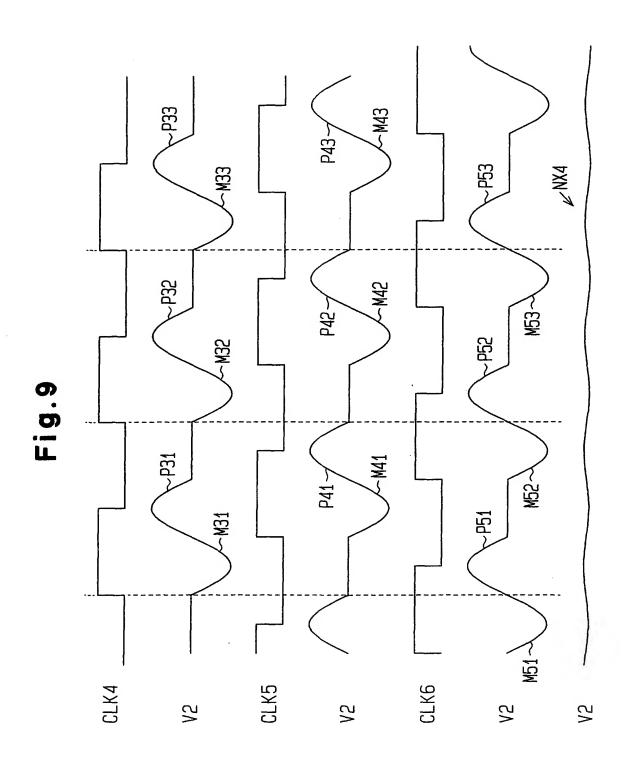
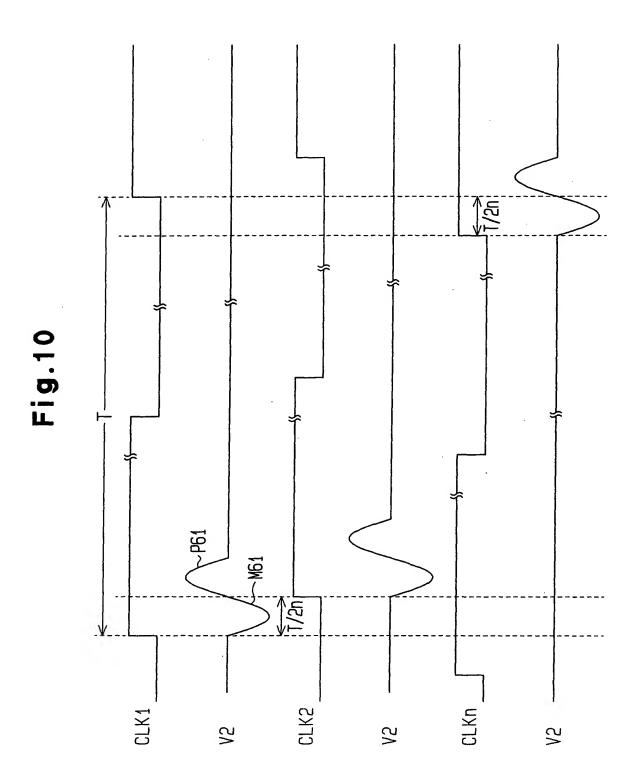


Fig.8







Title: DEVICE AND METHOD FOR INHIBITING POWER FLUCTUATION

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Fig.11

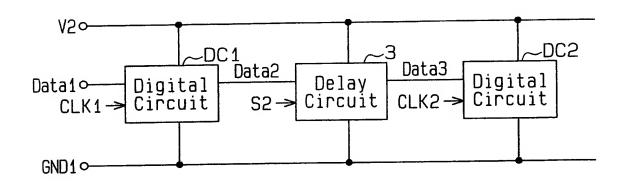


Fig. 12

